

FIG. 1

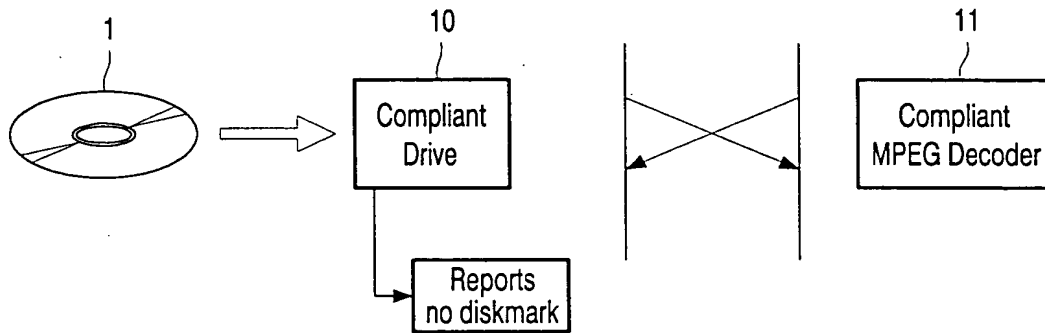


FIG. 2A

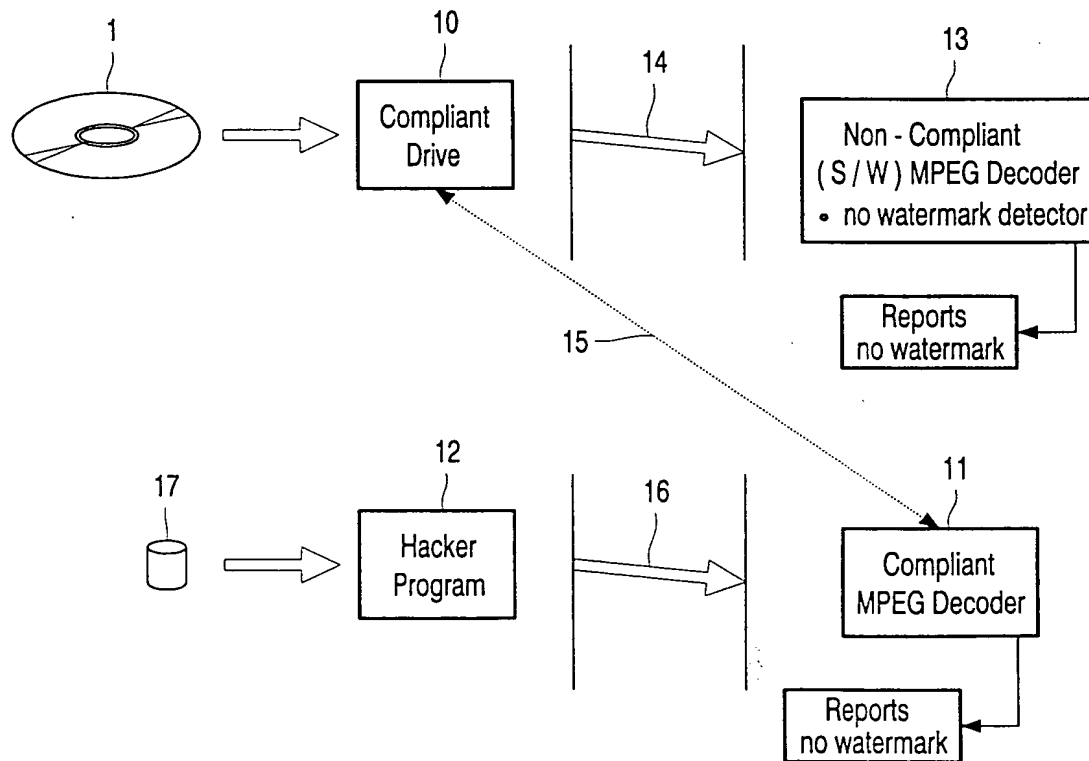


FIG. 2B

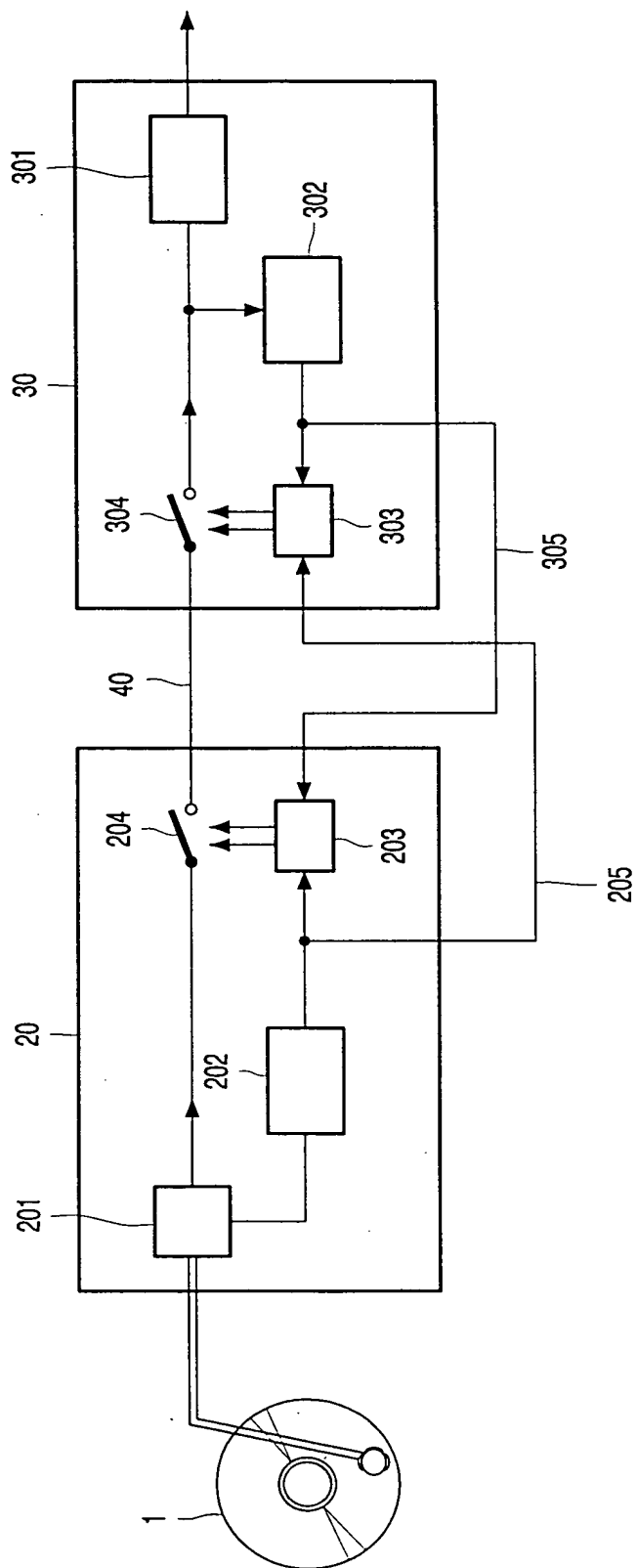


FIG. 3

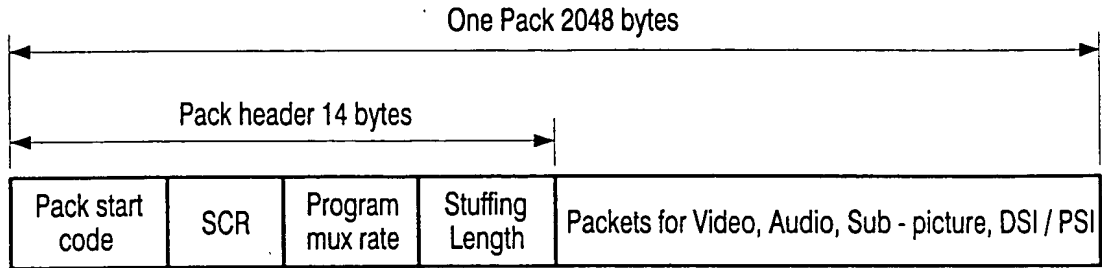


FIG. 4

Field	Number of bits	Value
pack_start_code	32	000001BA h
'01'	2	01 b
SCR_base [ 32 .. 30 ]	3	SCR_base [ 32 ] = 0
marker_bit	1	1 b
SCR_base [ 29 .. 15 ]	15	
marker_bit	1	1 b
SCR_base [ 14 .. 0 ]	15	
marker_bit	1	1 b
SCR_extension	9	
marker_bit	1	1 b
program_mux_rate	22	0189C3 x
marker_bit	1	1 b
marker_bit	1	1 b
Reserved	5	11111 b
pack_stuffing_length	3	

FIG. 5

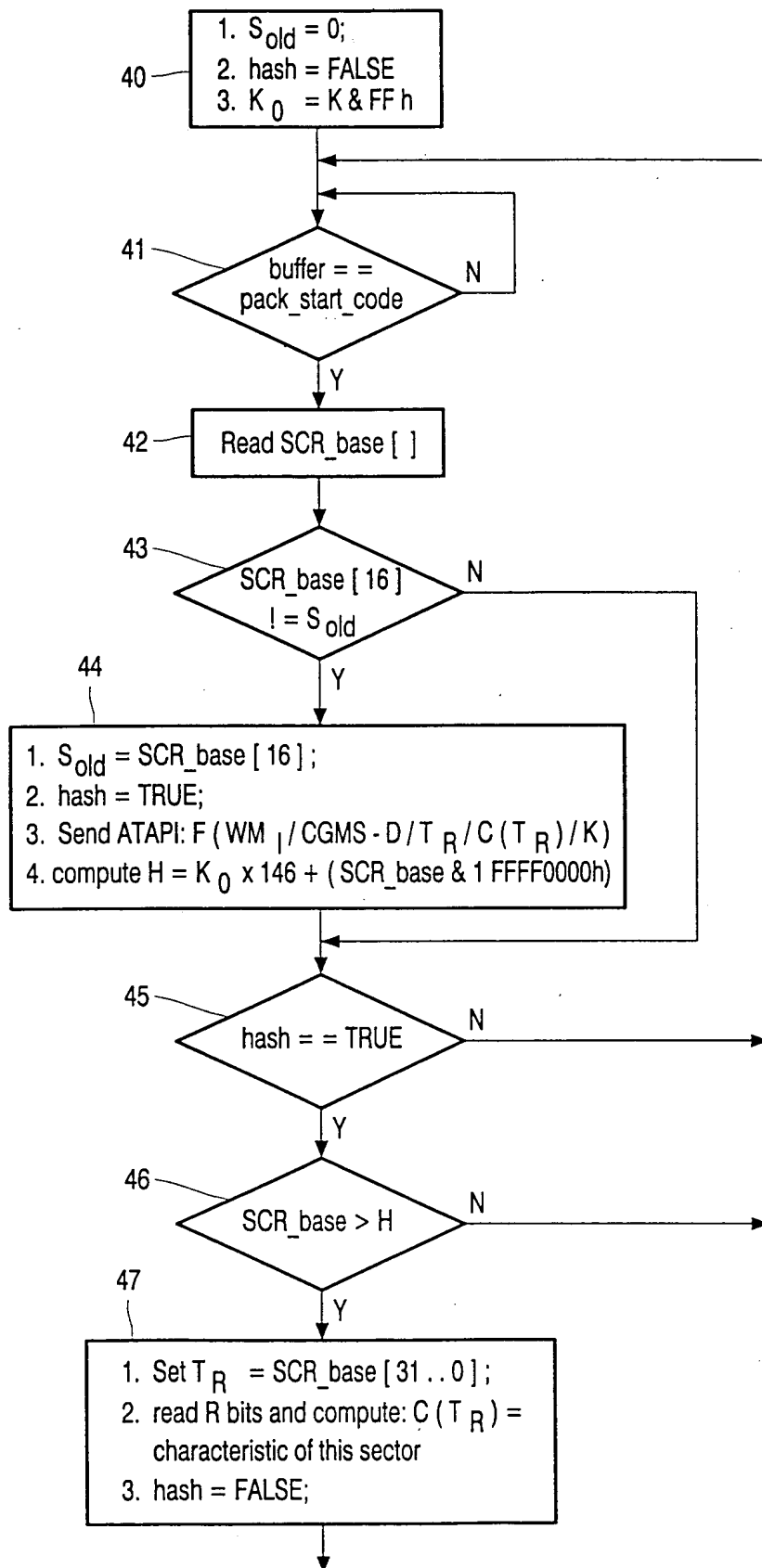


FIG. 6